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TROP, PRUNER & HU, P.C.

INTELLECTUAL PROPERTY LAW ATTORNEYS

1616 South Voss Road, Suite 750
Houston, Texas 77057-2631

Bus: (713) 468-8880

Fax: (713) 468-8883

Fax

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|-----------------|--------------------------|----------------|---------------------------|
| To: | Examiner James R. Harvey | From: | Timothy N. Trop |
| Company: | US Patent Office | Date: | August 21, 2006 |
| Fax: | (571) 273-8300 | Pages: | 7 (including cover sheet) |
| Your Re: | 10/822,572 | Our Re: | ITL1113US |

☐ Urgent ☐ For Review ☐ Please Comment ☐ Please Reply ☐ Confirm Receipt

MESSAGE:

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AUG 21 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | |
|------------------------------------|---|-----------------------------|
| In re Applicant: | § | |
| Shawn L. Lloyd et al. | § | Art Unit: 2833 |
| | § | |
| Serial No.: 10/822,572 | § | Examiner: James R. Harvey |
| | § | |
| Filed: April 12, 2004 | § | Docket: ITL.1113US |
| | § | P18785 |
| For: Making Electrical Connections | § | |
| Between a Circuit Board and | § | Assignee: Intel Corporation |
| an Integrated Circuit | § | |

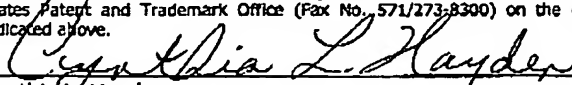
Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed August 7, 2006, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: August 21, 2006
I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No., 571/273-8300) on the date indicated above.

Cynthia L. Hayden